



FORM PTO-1449	SERIAL NO. 10/612,083	CASE NO. 14228US01
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE July 2, 2003	GROUP ART UNIT 2631
(use several sheets if necessary)		APPLICANT(S): Kevin T. Chan

## REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
	3,601,544	08/24/1971	Murphy	179/15BM	04/16/1969
	3,963,990	06/15/1976	DiFonzo	325/476	02/27/1974
	4,682,358	07/21/1987	Werner	379/411	12/4/1984
	5,204,880	04/20/1993	Wurster et al.	375/36	04/23/1991
	5,267,269	11/30/1993	Shih et al.	375/60	09/04/1991
	5,325,400	06/28/1994	Co et al.	375/60	06/04/1992
	5,399,996	03/21/1995	Yates et al.	331/74	08/16/1993
	5,600,321	02/04/1997	Wincn	341/144	06/07/1995
	5,640,605	06/17/1997	Johnson et al.	395/881	08/26/1994
	5,651,029	07/22/1997	Yang et al.	375/296	05/16/1995
	5,745,564	04/28/1998	Meek	379/410	01/26/1995
	5,798,661	08/25/1998	Runaldue et al.	327/106	02/09/1996
	5,896,417	04/20/1999	Lau	375/258	10/25/1996
	5,995,555	11/30/1999	Young	375/296	10/9/1997
	6,154,784	11/28/2000	Liu	709/250	06/10/1998
	6,160,851	12/12/2000	Brown et al.	375/254	02/26/1998
	6,332,004	12/18/2001	Chan	375/257	10/29/1999

## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
	0 673 139	09/1995	Europe		
	EP 0 763 917	03/1997	Europe		
	EP 0 863 640	09/1998	Europe		
	WO 92/11702	07/1992	PCT		

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)
	Huang et al. "Simulated Performance of 1000BASE-T Receiver with Different Analog Front End Designs", Solid-State Circuits Research laboratory Department of Electrical and Computer Engineering University of California, Davis, Davis, CA 95616 (4 pages)
	Sommer, "Ethernet transceiver offers upgrade from existing networks," 8029 electronic Engineering, April 1995, pp. 25-26, 28, 30
	Cherubini et al., "100Base-T2 : 100 Mbit/s Ethernet over Two Pairs of Category-3 Cabling," August 1997, pp. 1014-1018.
	Goldberg, "100Base-T4 Chip Brings Speed to Today's LANs," 2328 Electronic Design, February 1995, pp. 180-182

EXAMINER <i>F. Boacure</i>	DATE CONSIDERED <i>11/17/04</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.